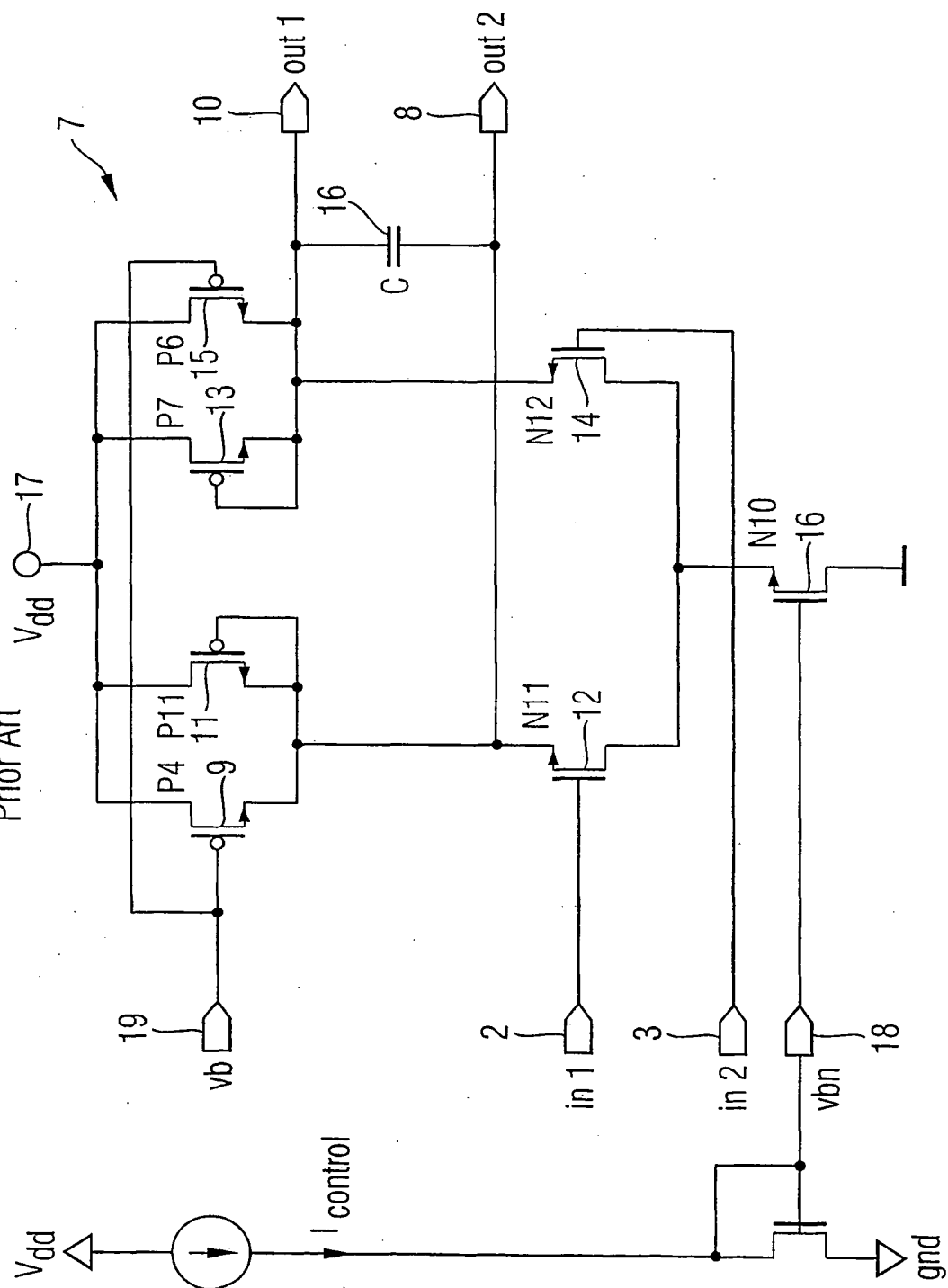


Prior Art



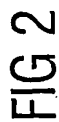
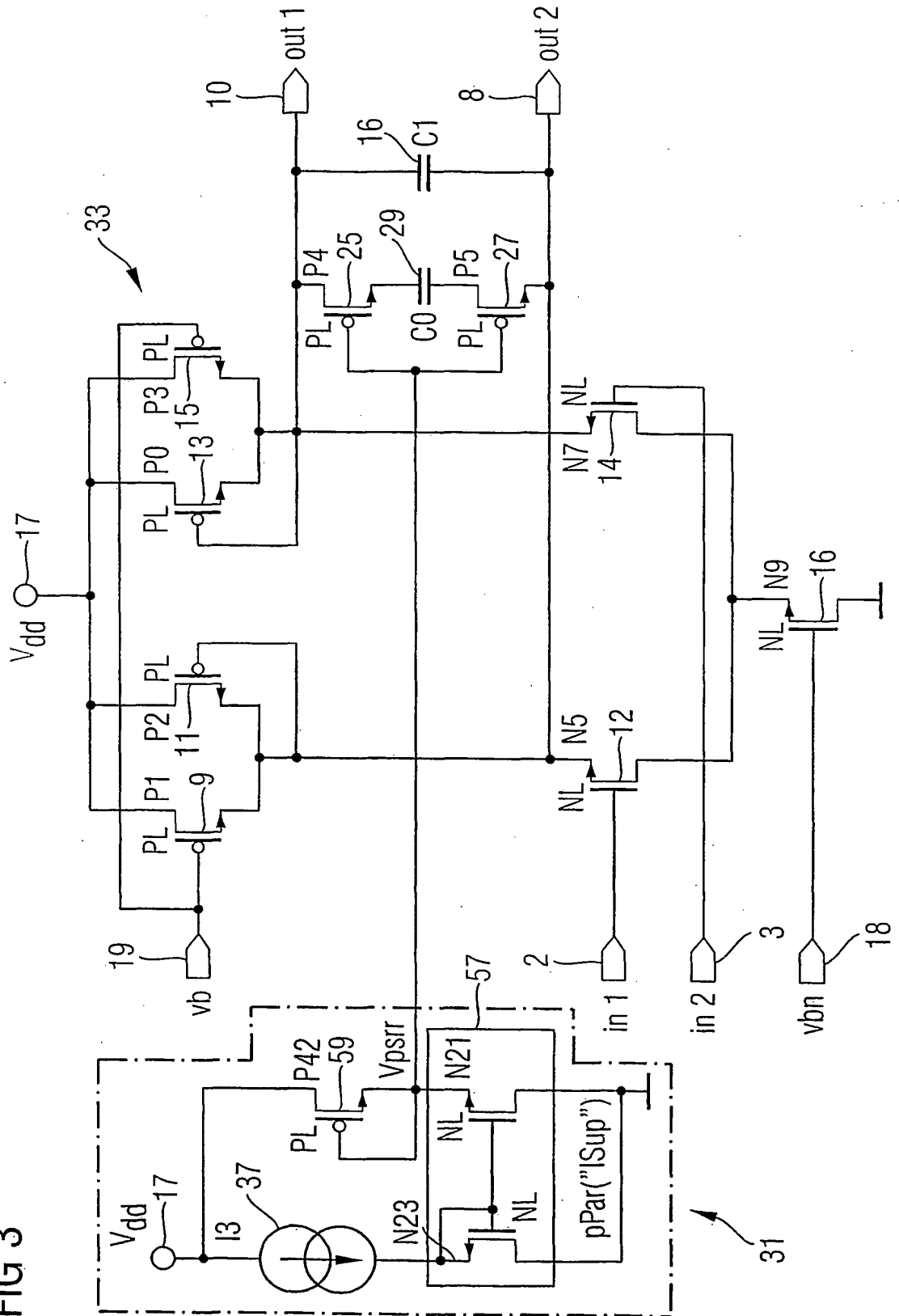


FIG 3



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FIG 4A

Prior Art

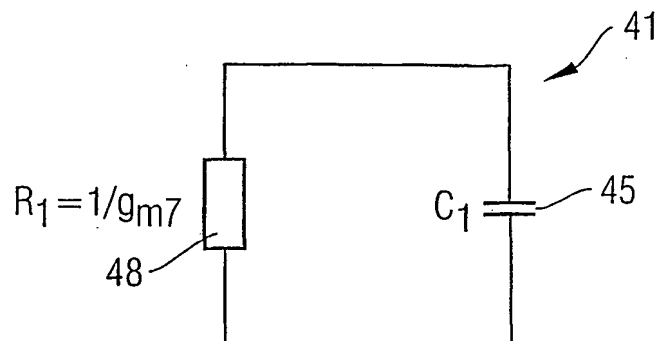


FIG 4B

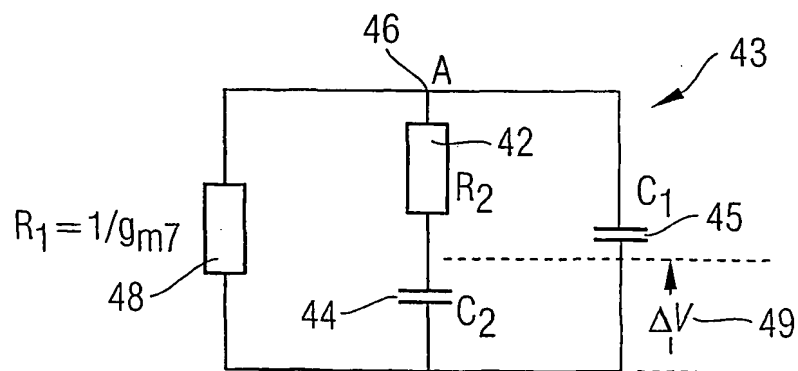
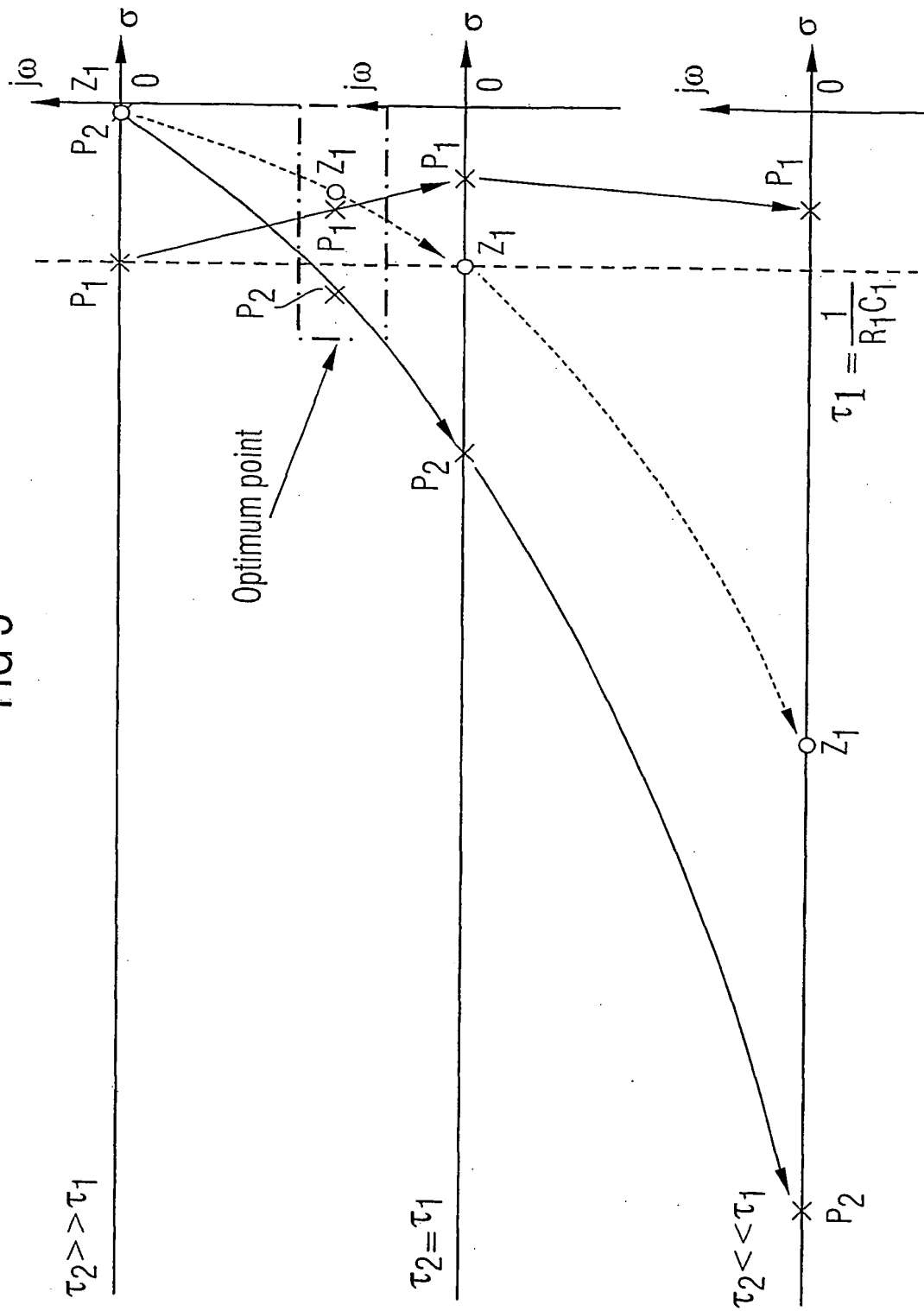


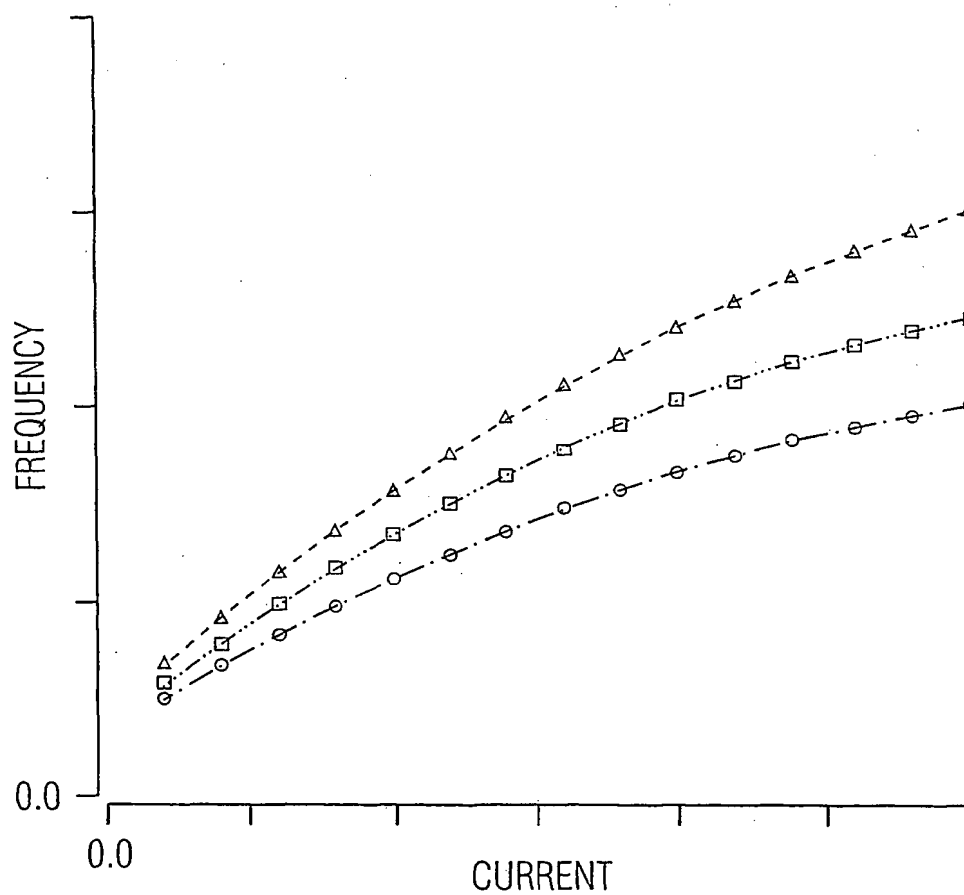
FIG 5



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FIG 6

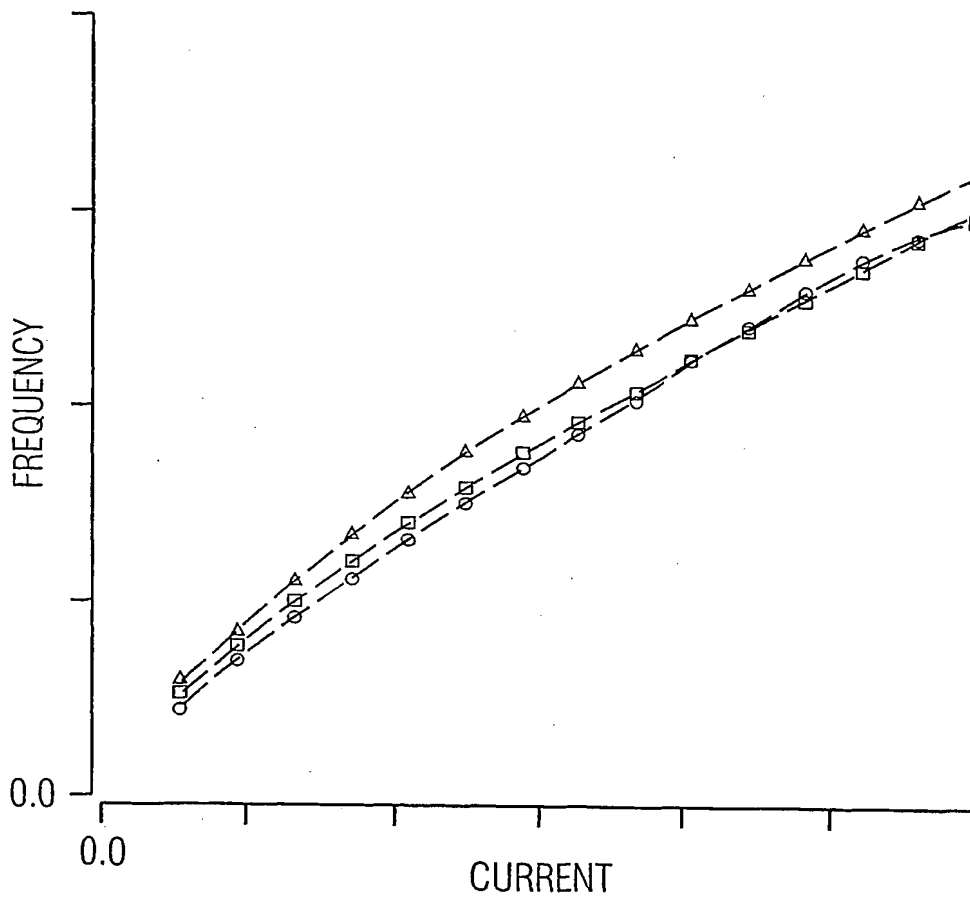
Prior Art



- traditional design SSH condition
- traditional design nominal condition
- △ traditional design FFL condition

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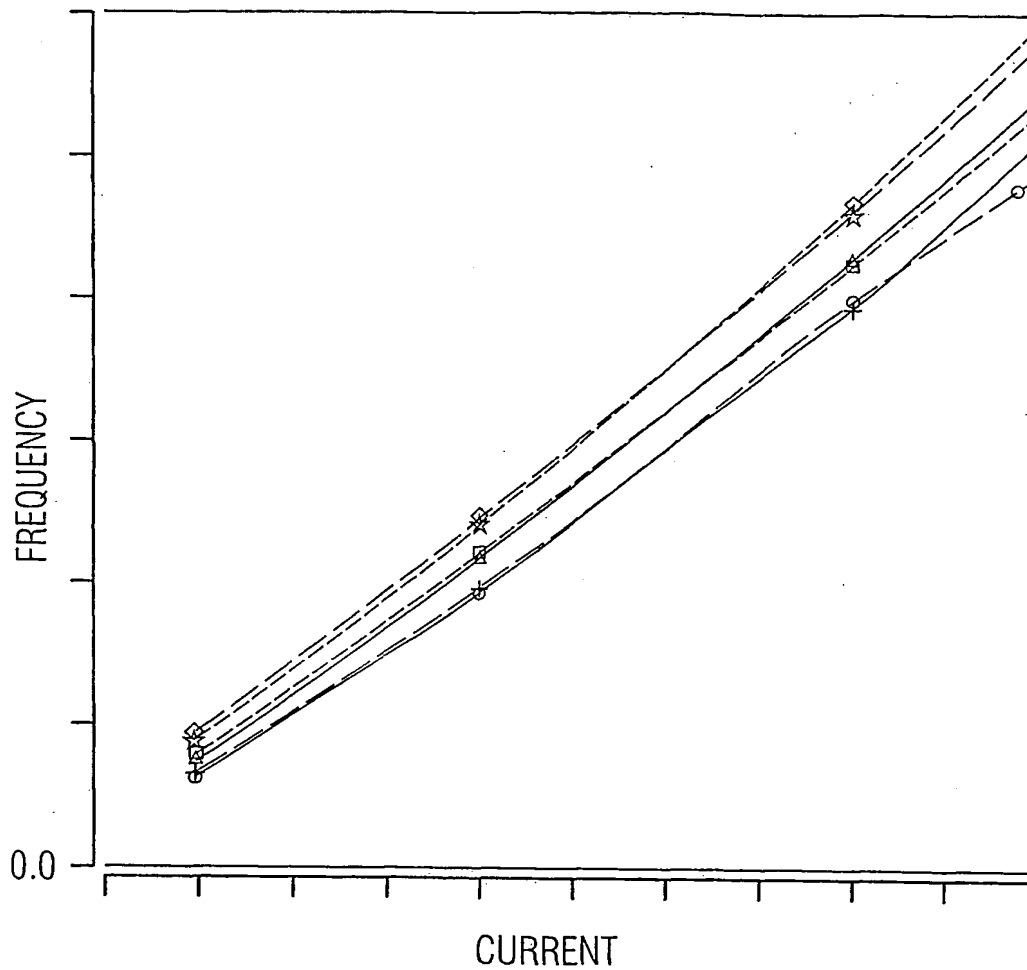
FIG 7



- — modified design SSH condition —
- △ — modified design FFL condition —
- — modified design nominal condition —

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FIG 8



- Freq(OUT1)-nom, 1.65V, 27 C, 10u CS
- △ Freq(OUT1)-nom, 1.35V, 27 C, 10u CS
- Freq(OUT1)-slow, 1.35V, 150 C, 8u CS
- ◇ Freq(OUT1)-fast, 1.65V, -50 C, 12u CS
- ☆ Freq(OUT1)-fast, 1.35V, -50 C, 12u CS
- + Freq(OUT1)-slow, 1.65V, 150 C, 8u CS

FIG 9

